

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|-----------------|------------------|---------|------------------|
| L1 | 10 | Infineon adj Technologies.as. and (float\$4 adj gate)with(nitride) | US-PGPUB; USPAT | OR | OFF | 2005/05/02 18:43 |
| L2 | 1 | Infineon adj Technologies.as. and (float\$4 adj gate)with(nitride) | EPO; JPO | OR | OFF | 2005/05/02 18:43 |
| L3 | 1 | Infineon adj Technologies.as. and (float\$4 adj gate)and(nitride) | EPO; JPO | OR | OFF | 2005/05/02 18:50 |
| L4 | 28 | Infineon adj Technologies.as. and (float\$4 adj gate)and trench\$4 | US-PGPUB; USPAT | OR | OFF | 2005/05/02 18:44 |
| L5 | 5 | Infineon adj Technologies.as. and (float\$4 adj gate)and trench\$4 | EPO; JPO | OR | OFF | 2005/05/02 18:46 |
| L6 | 4 | Infineon adj Technologies.as. and (charge\$2 near3 stor\$4)and(trench\$4) | EPO; JPO | OR | OFF | 2005/05/02 18:51 |
| L7 | 252 | Infineon adj Technologies.as. and (charge\$2 near3 stor\$4)and(trench\$4) | US-PGPUB; USPAT | OR | OFF | 2005/05/02 18:48 |
| L8 | 59 | Infineon adj Technologies.as. and (charge\$2 near3 stor\$4)near21(trench\$4) | US-PGPUB; USPAT | OR | OFF | 2005/05/02 18:49 |
| L9 | 13 | INFN.pac. and (float\$4 adj gate)and trench\$4 | DERWENT | OR | OFF | 2005/05/02 18:49 |
| L10 | 8226 | INFN.pac. | DERWENT | OR | OFF | 2005/05/02 18:49 |
| L11 | 4 | 10 and (float\$4 adj gate)and(nitride) | DERWENT | OR | OFF | 2005/05/02 18:50 |
| L12 | 7 | 10 and (charge\$2 near3 stor\$4)and(trench\$4) | DERWENT | OR | OFF | 2005/05/02 18:51 |